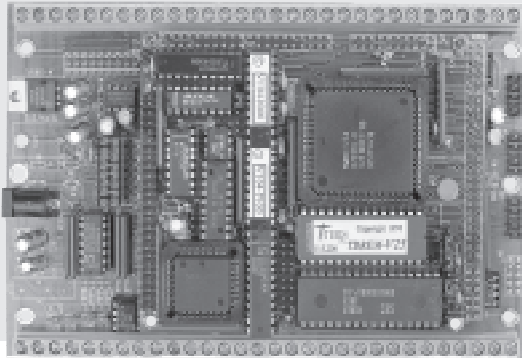


A-Drive™ (AD)

I/O Board

22 ADC inputs + 8 DAC outputs



Features:

- 6.2x4.2", -40degreesC to +80degreesC, Driven by V104™
- Up to 22 ch. 12-bit ADC, and up to 8 ch. 12-bit DAC
- 6 channels of high-gain signal conditioning
- Directly interfaces to thermocouples, RTD, or strain gauges
- On-board temperature sensor for cold-junction compensation

Ordering Information

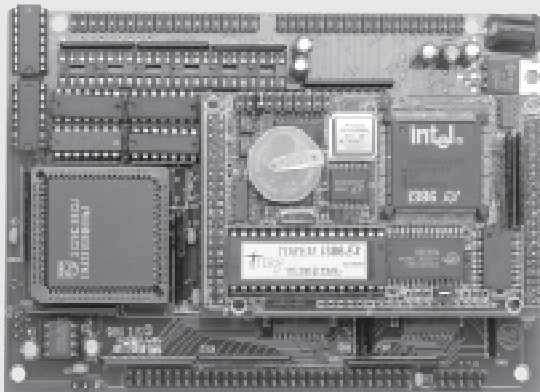
AD **\$299** **Qty 1**
 NOT including V104™. NOT including add-on options.

Add-on Options:

- 1) 4 ch. 12-bit DAC (MAX537) \$80
- 2) Upgrade precision ops up to 8 \$10 each

LittleDrive™ (LD)

10 UARTs, 22 solenoid drivers, 80 I/Os

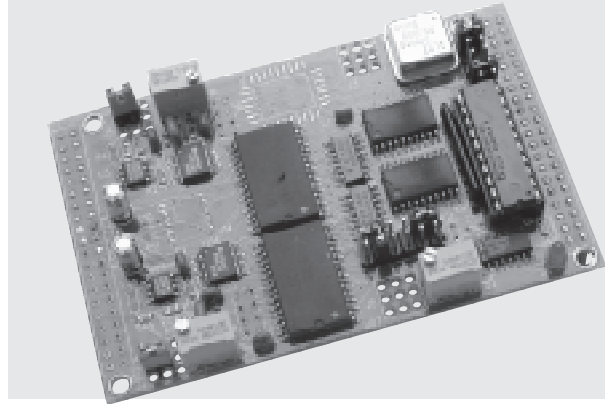


Features:

- 5.7x4.1", 80 I/O lines including 24x2 PPI I/O lines
 - Power consumption: < 200mA
 - Power input: +8.5 to +12V w/ linear regulator, or +8.5 to +35V w/ switching regulator *
 - 10 RS-232 serial ports; 6 of them configurable to RS-485 *
 - Octal UART • 14 solenoid sinking drivers, 8 sourcing
- * optional

Grabber™ (GR)

High Speed ADC and FIFO



Features:

- 2.3x3.6x0.5"
- 80MHz, 8-bit ADC, with front signal conditioning
- Driven by AEP/AE86P/A104/BBA/IEP/IEM/ID/586Engine
- 2KB 10ns FIFO for 80MHz ADC data acquisition
- Dual channel, 8-bit ADC and 55ns 512KB data buffer
- Clock selectable from on-board oscillator, external, or host
- Trigger window based on "one shot", from external or host
- Simultaneously high speed dual analog signal acquisition
- Image data, CCD frame data capture

The **Grabber™ (GR)** is a high-speed dual channel analog signal digitizer board. It features two 8-bit 80Mhz ADCs (AD9057, AnalogDevices) with front amplifiers (AD8041, AnalogDevices).

There are dual 8-bit ADCs and a 20-55ns 512KB SRAM can be installed to collect 512KB data for each ADC simultaneously. A single 12-bit high speed ADC is also optional. One channel 8-bit ADC data can be collected at an 80MHz sample rate with an optional 2KB 10ns FIFO (IDT72231, or CY7C4231).

The master clock can be selected by using an on-board oscillator, host clock, or externally. The ADC operates continually, but data can only be stored in the FIFO within a trigger window. The trigger signal can be generated by a host, or externally. The host controller can read and process the ADC data from the FIFO.

Ordering Information

Grabber **Call**

The **LittleDrive™** must be driven by an **i386-Engine™** or **A-Engine™**.

Ordering Information

LD **\$299** **Qty 1**

Includes: Octal UART, 24x2 PPI, solenoid drivers, RS-232 driver.

NOT including add-on options. IE/AE/586E is NOT included.

Add-on Options:

- 1) Switching regulator \$30
- 2) Low-power version \$25
- 3) RS-485 drivers up to 6 \$10 each



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