Modifications for LittleDrive to support 16-bit controllers: AE, AE86, IEM, 586E. March 8, 2006

Hardware Modification on AE:

- 1) cut trace on component side VCC to J2.10
- 2) add wire between J2.10 to J2.15=P3. NO SCC U8 installed!
- 3) add wire bring J1.23=CLK to J1.4=P1
- 4) distroy C0's one vias which is under the lable "C" to disconnect CLK to P1.

Hardware Modification on AE86:

- 1) cut trace on component side to J2.10(A19).
- 2) add wire between J2.10 to J2.15=P3. NO SCC U8 installed.

Hardware Modification on IEM:

- 1) cut pins of J1: pin 11, 13, 21, 23, 25, 27, 33, 35, 37, 39.
- 2) cut pins of J2.16(T6), J2.26(RxD1), J2.32(RxD0)
- 3) No SCC U8 be installed!
- 4) Do not install 5V reg. and J0 DC power jack.

Hardware Modifications on 586-Engine:

Wire J2.10=P7=/E to J4.1=/PITG2=/GPCS3 for SC26198, base address=0x2000

Modifications on the LittleDrive:

- 1) Install CCLK 3.6864 MHz crystal
- 2) Install PAL LDP586.JED, Cut off PAL pin 19.
- 3) add wire between J1.31=/RD to PAL U1.18
- 4) No pins on socket of J1, J2:

J1: 3, 11, 13, 21, 23, 25, 27, 33, 35, 37, 39 J2: 1, 7, 22, 25, 36