

Modifications for SerialDrive to support RS485 on SER0, SER1, and SCC. 10/16/2002

SER0 Modification:

SER0 is default RS232 for DEBUG. If you have to install RS485 driver for SER0, you have no RS232 debug.

- 1) Lift pin 9 of U16 (RS232 drivers, MAX232) from PCB.
- 2) Install RS485 8-pin DIP chip (TI 75LBC184) in the socket U18.
- 3) 386EX pin 102=P11 can be used as Transmit/receiving control (1/0) for the RS485 half-duplex control.

SER1Modification:

- 1) Lift pin 9 of U11 (RS232 drivers, MAX232D) from PCB.
- 2) Install RS485 8-pin DIP chip (TI 75LBC184) in the socket U15.
- 3) Lift pin 3 of the RS485 driver chip out of the 8-pin U15 socket.
- 4) Add wire from the lifted pin 3 of the RS485 driver chip, to J5 pin 17=/RTS1. Use /RTS1 as RS485 half-duplex control.

SCC Modification:

- 1) Do not install U19 RS232 driver.
- 2) Install RS485 8-pin DIP chip (TI 75LBC184) in the socket U17.